



Attorney Docket No. 09792909-5755

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:	)	Group Art Unit:
	)	
Tsutomu IMOTO	)	Examiner:
	)	
Application No. 10/689,305	)	
	)	
Filed: October 20, 2003	)	
	)	
For: Method of Fabricating Semiconductor	)	
Device Having Semiconductor	)	
Resistance Element	)	

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT  
WITHIN THREE MONTHS OF FILING OR  
BEFORE MAILING OF FIRST OFFICE ACTION (37 C.F.R. section 1.97(b))**

**IDENTIFICATION OF TIME OF FILING THE ACCOMPANYING  
INFORMATION DISCLOSURE STATEMENT**

The information disclosure statement submitted herewith is being filed within three months of the filing date of the application or date of entry into the national stage of an international application or before the mailing date of a first Office action on the merits, whichever event occurs last. 37 C.F.R. section 1.97(b).

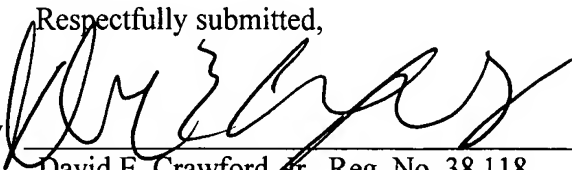
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Transmittal of Information Disclosure Statement  
Page 2

**A copy of cite number 14 is enclosed herewith. Copies of cite numbers 1-13 were cited in and can be found in parent application serial number 09/862,042.**

The filing of this information disclosure statement shall not be construed as an admission against interest in any manner. Notice of January 9, 1992, 1135 O.G. 13-25, at 25.

Dated: 15 DEC 03

Respectfully submitted,  
  
By \_\_\_\_\_  
David E. Crawford, Jr., Reg. No. 38,118  
Customer No. 26263  
314.259.5810



<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)  Sheet 1 of 2				<b>Complete if Known</b>	
				Application Number	10/689,305
				Filing Date	October 20, 2003
				First Named Inventor	Tsutomu Imoto
				Group Art Unit	
				Examiner Name	
				Attorney Docket Number	09792909-5755

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1	US-4,298,401	11-03-1981	Nuez et al.	
	2	US-4,228,450	10-14-1980	Anantha et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Number Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
	3	JP 4-03-169063	07-22-1991	Matsuoka		<input type="checkbox"/>
	4	JP 4-04-067666	03-03-1992	Segawa et al.		<input type="checkbox"/>
	5	JP 4-05-114699	05-07-1993	Otsuki		<input type="checkbox"/>
	6	JP 4-05-315547	11-26-1993	Fujii		<input type="checkbox"/>
	7	JP 4-01-268049	10-25-1989	Yamaguchi		<input type="checkbox"/>
	8	JP 3-62-143478	06-26-1987	Nishii		<input type="checkbox"/>
	9	JP 3-62-060252	03-16-1987	Hosono		<input type="checkbox"/>
	10	JP 3-61-002361	01-08-1986	Akamatsu		<input type="checkbox"/>
	11	JP 3-57-018354	01-30-1982	Ishibashi		<input type="checkbox"/>
	12	JP 3-54-056777	05-08-1979	Umehara		<input type="checkbox"/>
	13	JP 3-53-126875	11-06-1978	Masuda et al.		<input type="checkbox"/>

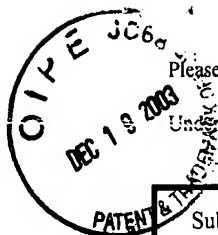
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				Application Number	10/689,305
				Filing Date	October 20, 2003
				First Named Inventor	Tsutomu Imoto
				Group Art Unit	
Sheet	2	of	2	Examiner Name	
				Attorney Docket Number	09792909-5755

OTHER ITEMS – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	14	GOTO et al., "Two Dimensional Numerical Simulation of Side-Gating Effect in GaAs MESFET's," IEEE, 1990, pp. 1821-1827, Vol. 37.	

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